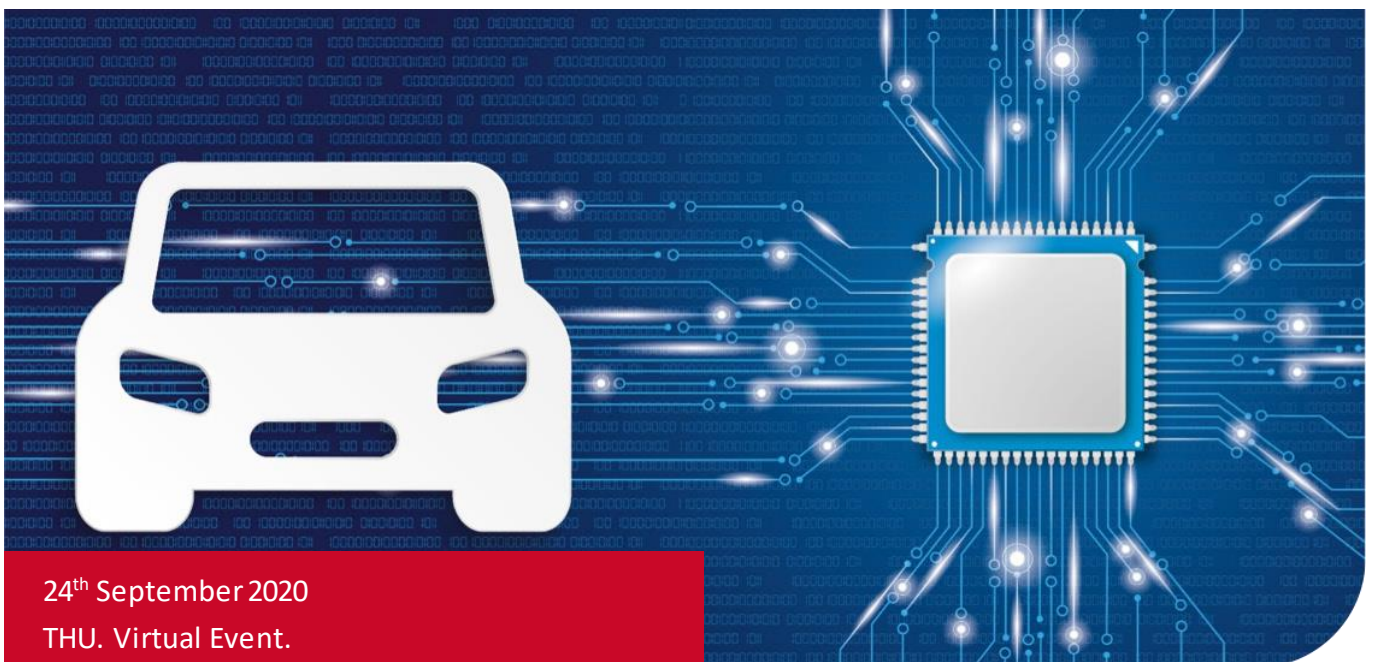


# PROGRAMMABLE PROCESSING FOR THE AUTONOMOUS / CONNECTED VEHICLE

– FROM CLASSICAL FPGA TO ADAPTABLE COMPUTING

ALGORITHMS. ARCHITECTURE. REALIZATION. TEST.



Time (CEST)	PROGRAM
09:45 - 10:00	<b>Registration</b>
10:00 – 10:20	<b>Opening Remarks from the Program Committee</b> Anestis Terzis, THU, Endric Schubert, MLE, Mathias Güthoff, Xilinx
10:20 - 11:10	<b>FPGA and SoC Design using MATLAB and Simulink for Functional Safety</b> Dimitri Hamidi, The MathWorks GmbH
11:10 - 11:40	<b>PCIe-over-TCP-over-TSN-over-10/25 Gig Ethernet</b> Endric Schubert, Missing Link Electronics
11:40 - 12:10	<b>Mirror Replacement System - A FPGA-4-ADAS Story</b> Stefan Schütz, Solectrix GmbH
12:10 - 13:00	<b>Break</b>
13:00 - 13:30	<b>Overview: Xilinx in Automotive</b> , Mathias Güthoff, Xilinx <b>Automotive System Architectures from ADAS to AD</b> , Ralf Neuhaus, Xilinx
13:30 - 14:00	<b>Xilinx in AI – Versal AI-core, AI-Engine Architecture, Design Flow</b> Daniele Bagni, Xilinx
14:00 - 14:30	<b>Porting a Gesture Recognition Neural Network composed of CNN and LSTM to a FPGA-SoC</b> R. Briegel, JacoL FPGA Entwicklungen GmbH
14:30 - 15:00	<b>HAPPI-Net: Hardware-aware performant perception of Neural Networks</b> Alexander Frickenstein, BMW Group
15:00 - 15:30	<b>Security solution based on FPGA/SoC</b> Ralf Neuhaus, Xilinx



**Registration:** <https://innosued.de/workshop-programmable-processing-for-the-autonomous-connected-vehicle/>

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Partner



**innosüd**

**XILINX**

**mle**  
missing link electronics



## **SPEAKERS OF THE PREVIOUS EVENTS:**

### **3<sup>rd</sup> Workshop in 2019:**

**Martin Arend**, General Manager E/E Architecture and Technologies, BMW Group

**Prof. Dr. Elmar Cochlovius**, Furtwangen University

**Dr. Andrea Tomatis**, Director Engineering, Soft-in

**Andreas Schuler**, Director Applications, Missing Link Electronics

**Sebastian Stiemke**, Director Operations, Missing Link Electronics

**Wolfgang Vieweger**, Senior Manager, ZF Friedrichshafen AG

**Dr. Chris Kachris**, CEO and co-founder at InAccel

**Steffen Jannik Maier**, Researcher, Institute of Communication Technology, THU

**Johannes Schäfer**, Student (B. Eng.-Candidate), Institute of Communication Technology, THU

**Michaela Blott**, Distinguished Engineer, Xilinx Research

**Benedikt Schweizer**, Akademischer Mitarbeiter, Ulm University

### **2<sup>nd</sup> Workshop in 2018:**

**Wolfgang Fey**, Sen. Manager Assisted Driving Control Systems, Continental

**Dr. Karsten Trott**, FAE, Xilinx

**Felix Eberli**, Department Head, Supercomputing Systems AG

**Dr. Petros Kapsalas**, Computer Vision Expert, Panasonic Automotive & Industrial Systems

**Jochen Frensch**, Lead Engineer, Mentor Embedded, Mentor Graphics Development  
(Deutschland) GmbH

**Mark Leznik**, Researcher, Institute of Information Resource Management, Ulm University

**Philipp Hoffmann**, Project Manager Interior Concepts Entry Class Vehicles, BMW Group

**Steffen Jannik Maier**, Researcher, Ulm University of Applied Sciences

**Tobias Augspurger**, Group Leader Department Autonomous Logistics, StreetScooter GmbH

### **1<sup>st</sup> Workshop in 2017:**

**Prof. Dr. Anestis Terzis**, Ulm University of Applied Sciences

**Felix Eberli**, Supercomputing Systems AG

**Jun.-Prof. Dr. Stephan Reuter**, Ulm University

**Prof. Dr. Rainer Lienhart**, Augsburg University

**Nick Ni**, Xilinx

**Vinod Kathail**, Xilinx

**Kay Migge**, Xilinx