

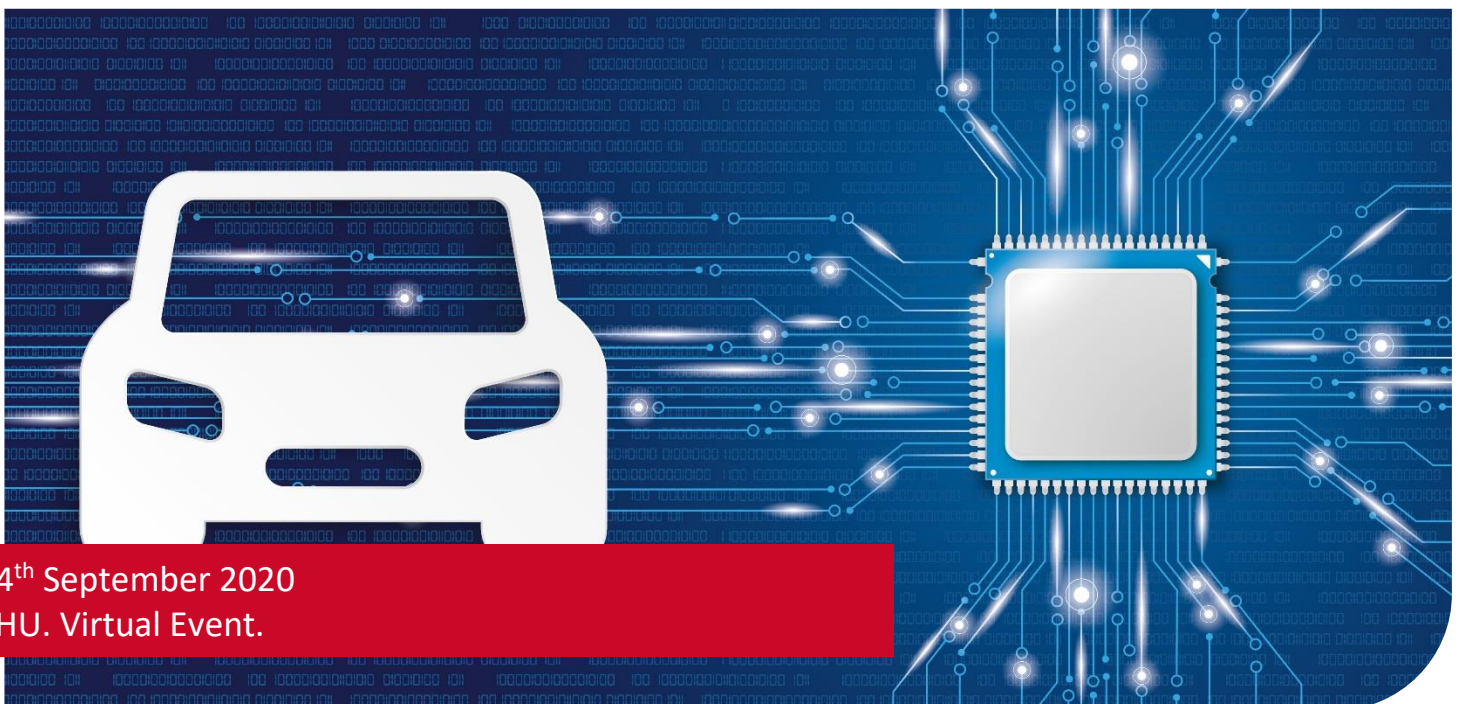
Call for Contributions

4th Workshop

PROGRAMMABLE PROCESSING FOR THE AUTONOMOUS / CONNECTED VEHICLE

– FROM CLASSICAL FPGA TO ACAP

ALGORITHMS. ARCHITECTURE. REALIZATION. TEST.



24th September 2020
THU. Virtual Event.

The Workshop PROGRAMMABLE PROCESSING FOR THE AUTONOMOUS / CONNECTED VEHICLE – FROM CLASSICAL FPGA TO ACAP, is coming up again on September 24th, 2020. For the first time, it's available as a 100 % online experience!

This workshop is the ideal event to keep up to date with the most recent achievements in the fields of processing technologies, digital systems design, challenges and applications for the Autonomous and Connected Vehicle. The covered topics are an active area of research and development in both academia and industry.

Through its broad multidisciplinary and highly interactive character, it offers ideal opportunities to network and interact with local and international experts from the industrial and academic fields.

We are soliciting contributions in (but not limited to) the following topics:

- FPGA, MPSoC and ACAP for the Autonomous / Connected Vehicle
- AI for the Autonomous / Connected Vehicle
- Processing Technologies for AD/ADAS
- Design using C/C++/SystemC
- Design Tools and Design Methodologies
- Heterogeneous and Cloud Computing
- Safety-compliant Processing (e.g. ISO 26262)
- Electromagnetic Compatibility (EMC) of Processing Circuits
- Algorithms for Automated Driving
- Architectures for the Intelligent Connected Vehicle
- Machine Vision / Computer Vision
- Deep Neural Networks / Deep Learning
- Realization and Test



Program Committee: Prof. Dr. A. Terzis, Dr. E. Schubert, M. Güthoff **Conference Language:** German and English

Contact: Prof. Dr. A. Terzis, Eberhard-Finckh-Straße 11, 89075 Ulm, Fon: +49 (0731) 50-28341, anestis.terzis@thu.de

Partner

Important Dates:

1. Abstract Submission Deadline: 17.07.2020
2. Acceptance Notification: 31.07.2020
3. Camera Ready Contribution (paper or presentation): 07.09.2020
4. Workshop: 24.09.2020

Submission and Registration Address:
fpga2020@missinglinkelectronics.com

Requirements for submission:

Contact data incl. biography/CV, presentation title and abstract of the contribution (max. 500 words).

The contributions will be published in the digital conference proceedings and online with ISBN.

Impressions:

<http://www.missinglinkelectronics.com/fpga2017>

<http://www.missinglinkelectronics.com/fpga2018>

<http://www.missinglinkelectronics.com/fpga2019>

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Partner



Speakers of the previous events

3rd Workshop in 2019:

Martin Arend, General Manager E/E Architecture and Technologies, BMW Group
Prof. Dr. Elmar Cochlovius, Furtwangen University
Dr. Andrea Tomatis, Director Engineering, Soft-in
Andreas Schuler, Director Applications, Missing Link Electronics
Sebastian Stiemke, Director Operations, Missing Link Electronics
Wolfgang Vieweger, Senior Manager, ZF Friedrichshafen AG
Dr. Chris Kachris, CEO and co-founder at InAccel
Steffen Jannik Maier, Researcher, Institute of Communication Technology, THU
Johannes Schäfer, Student (B.Eng.-Candidate), Institute of Communication Technology, THU
Michaela Blott, Distinguished Engineer, Xilinx Research
Benedikt Schweizer, Akademischer Mitarbeiter, Ulm University

2nd Workshop in 2018:

Wolfgang Fey, Sen. Manager Assisted Driving Control Systems, Continental
Dr. Karsten Trott, FAE, Xilinx
Felix Eberli, Department Head, Supercomputing Systems AG
Dr. Petros Kapsalas, Computer Vision Expert, Panasonic Automotive & Industrial Systems
Jochen Frensch, Lead Engineer, Mentor Embedded, Mentor Graphics Development (Deutschland) GmbH
Mark Leznik, Researcher, Institute of Information Resource Management, Ulm University
Philipp Hoffmann, Project Manager Interior Concepts Entry Class Vehicles, BMW Group
Steffen Jannik Maier, Researcher, Ulm University of Applied Sciences
Tobias Augspurger, Group Leader Department Autonomous Logistics, StreetScooter GmbH

1st Workshop in 2017:

Prof. Dr. Anestis Terzis, Ulm University of Applied Sciences
Felix Eberli, Supercomputing Systems AG
Jun.-Prof. Dr. Stephan Reuter, Ulm University
Prof. Dr. Rainer Lienhart, Augsburg University
Nick Ni, Xilinx
Vinod Kathail, Xilinx
Kay Migge, Xilinx